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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/781,559	02/18/2004	Ross E. Tegatz	TI-36747	9295

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TEXAS INSTRUMENTS INCORPORATED
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EXAMINER

HAROON, ADEEL

ART UNIT	PAPER NUMBER
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2618

NOTIFICATION DATE	DELIVERY MODE
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02/25/2008

ELECTRONIC

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

uspto@ti.com
uspto@demail.itg.ti.com

Office Action Summary	Application No. 10/781,559	Applicant(s) TEGGATZ ET AL.	
	Examiner Adeel Haroon	Art Unit 2618	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-35 is/are pending in the application.
- 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) ____ is/are allowed.
- 6) ☒ Claim(s) 1-35 is/are rejected.
- 7) ☐ Claim(s) ____ is/are objected to.
- 8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 18 February 2004 is/are: a) ☐ accepted or b) ☒ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. ____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|--|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. ____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)
Paper No(s)/Mail Date ____ | 6) <input type="checkbox"/> Other: ____ |

DETAILED ACTION

Drawings

1. Figure 1 should be designated by a legend such as --Prior Art-- because only that which is old is illustrated. See MPEP § 608.02(g). Corrected drawings in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. The replacement sheet(s) should be labeled "Replacement Sheet" in the page header (as per 37 CFR 1.84(c)) so as not to obstruct any portion of the drawing figures. If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance. In the brief description of the drawings section, Figure 1 is described as "an illustration of one embodiment of a resonant circuit without benefit of the present invention", which conveys that it is prior art and not an invention of the applicant. Consequently, it was treated as prior art for the examination of the claims.

Claim Rejections - 35 USC § 112

2. The following is a quotation of the first paragraph of 35 U.S.C. 112:

The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.

3. Claims 20 and 30 are rejected under 35 U.S.C. 112, first paragraph, as failing to comply with the written description requirement. The claim(s) contains subject matter which was not described in the specification in such a way as to reasonably convey to one skilled in the relevant art that the inventor(s), at the time the application was filed, had possession of the claimed invention. Paragraph 34 of the specification describes the capacitors as being discrete but these claims state the capacitors are integrated in the semiconductor device. No other parts of the specification describe these capacitors as being integrated in the specification as evidenced by all figures, which all have the capacitors as discrete components; therefore, the specification does not reasonably convey how these capacitors are integrated in the semiconductor device.

Claim Rejections - 35 USC § 103

4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

5. Claims 1-19, 21-29, and 31-35 are rejected under 35 U.S.C. 103(a) as being unpatentable over Applicant's admitted prior art in view of Bowers et al. (U.S. 5,926,093).

With respect to claim 1, Applicant's figure 1, which is prior art, disclose a load, 104; a primary component, 106; and a secondary component array, 116, in parallel to the primary component (Paragraph 19). Applicant's figure 1 does not disclose a reduction system. However, Bowers et al. disclose a driver circuit connected to a resonant structure thus making it analogous art since it is in the same field of endeavor. Bowers et al. teach a reduction system, elements 118, that connect a driver circuit to a resonant circuit in figure 7a (Column 6, lines 24- 38). Therefore, it would be obvious to one of ordinary skill in the art at the time of the applicant's invention, to add the reduction system as taught by Bowers et al. to a resonant structure in order to better match the impedance from the driver circuit to the resonant circuit (Column 5, lines 4-9).

With respect to claim 2, Applicant's figure 1 shows the load being an antenna (Paragraph 19).

With respect to claims 3 and 4, element number 106 is a capacitor (Paragraph 19).

With respect to claims 5 and 6, element 116 is a capacitive array (Paragraph 19).

With respect to claims 8 and 9, Applicant's figure 1 discloses transistors, 122, as switchable elements (Paragraph 19).

With respect to claim 10, Applicant's figure 1, which is prior art, disclose a driver circuit, 110, initiated within a first integrated semiconductor device; a primary resistive element, 102; a primary capacitive element, 106; and a secondary component array, 116 (Paragraph 19). Applicant's figure 1 does not disclose a reduction system.

However, Bowers et al. disclose a driver circuit connected to a resonant structure thus making it analogous art since it is in the same field of endeavor. Bowers et al. teach a reduction system, elements 118, that connect a driver circuit to a resonant circuit in figure 7a (Column 6, lines 24- 38). Therefore, it would be obvious to one of ordinary skill in the art at the time of the applicant's invention, to add the reduction system as taught by Bowers et al. to a resonant structure in order to better match the impedance from the driver circuit to the resonant circuit (Column 5, lines 4-9).

With respect to claims 11 and 12, both Applicant's figure 1 and Bowers et al. disclose the resonant structure as a radio frequency resonant circuit.

With respect to claims 13 and 14, element 102 is a resistor (Paragraph 19).

With respect to claims 15 and 16, Applicant's figure 1 shows the load being an antenna (Paragraph 19).

With respect to claims 17-19, element number 106 is a capacitor (Paragraph 19).

With respect to claims 21 and 24, element 116 is a capacitive array (Paragraph 19).

With respect to claims 22 and 23, Applicant's figure 1 discloses transistors, 122, as switchable elements (Paragraph 19).

With respect to claims 25 and 26, Bowers et al. teach the capacitor and transistor as being integrated on a semiconductor device as evidenced by box 10 in figure 7a.

With respect to claim 27 and 29, Bowers et al. teach element 118 is a capacitor (Column 6, lines 24- 38).

With respect to claim 28, the examiner takes Official Notice that replacing one capacitor with a plurality of capacitors is extremely well known in the art. Therefore, it would be obvious to one of ordinary skill in the art to have a plurality of capacitors instead of just one capacitor in element 118 in order to provide a better impedance match and a better capacitance value.

With respect to claim 31, Applicant's figure 1, which is prior art, disclose a driver circuit, 110, initiated within a first integrated semiconductor device; a primary resistive element, 102; a primary capacitive element, 106; and a secondary component array, 116 (Paragraph 19). Applicant's figure 1 does not disclose a reduction system. However, Bowers et al. disclose a driver circuit connected to a resonant structure thus making it analogous art since it is in the same field of endeavor. Bowers et al. teach a reduction system, elements 118, that connect a driver circuit to a resonant circuit in figure 7a (Column 6, lines 24- 38). Therefore, it would be obvious to one of ordinary skill in the art at the time of the applicant's invention, to add the reduction system as taught by Bowers et al. to a resonant structure in order to better match the impedance from the driver circuit to the resonant circuit (Column 5, lines 4-9).

With respect to claims 32-35, Bowers et al. teach the capacitor and transistor as being integrated on a semiconductor device as evidenced by box 10 in figure 7a.

Conclusion

6. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Wuidart (U.S. 2003/0169169) disclose a tunable resonant circuit for an antenna. Humphrey (U.S. 2004/0246074) discloses a resonant filter with a capacitor array. Kennedy et al. (U.S. 2004/01202171) disclose a RF antenna with a connected resonant isolator. van Rumpt (U.S. 6,922,550) discloses a communication device with efficient excitation of a resonant circuit. Ballweber et al. (U.S. 6,889,036) disclose an integrated frequency selectable resonant coupling network. Sim (U.S. 2003/0132819) discloses an RF input stage with resonant circuits.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Adeel Haroon whose telephone number is (571) 272-7405. The examiner can normally be reached on Monday thru Friday, 8:30 a.m. - 5:00 p.m..

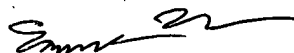
If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Edward Urban can be reached on (571) 272-7899. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

AH
2/15/08


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